

M.Tech. (Embedded Systems) -Syllabus for Entrance Test

Entrance test Date 24th April 2021 Time 2:00 pm to 3:00 pm followed by Interview

Pattern	MCQ Multiple Choice Question
Max Marks	50
Time Allotted	1 Hour
Entrance test is based on subjects of Electronics Engineering	Semiconductor Devices and Circuit Applications
	Network Analysis and Synthesis
	Digital Electronics
	Signals and Systems
	Control Systems
	Analog Circuit Design
	Electromagnetic Field Theory
	Principal of Communication
	Digital Communication
	Microwave and Radar
	Microcontroller
	Digital Signal Processing
	Antenna and wave Propagation
	Electronic Measurement and Instrumentation
	VLSI
	Fundamentals of Electronic Devices and Circuits
	Power Electronics
	Embedded System Design
	Microprocessor (8085)
Computer Communication network	
Mechatronics	